EPT USB FPGA Dev Sys Getting Started Guide

## Thanks for purchasing the DueProLogic FPGA Development System.

We have recently upgraded the DueProLogic software. This new software release makes the DueProLogic incompatible with the previous releases of software. This incompatibility is due to a change in the EEPROM attached to the FT2232H chip. The board hardware has not changed and still contains all the great features users have come to expect.

- Altera Cyclone IV FPGA
- 54 Inputs/Outputs 3.3V only
- High Speed bi-directional USB data communications with PC
- On Board Flash programming (for configuration of FPGA)
- 36 user LEDs
- Two push buttons
- SD Card connector

If there are any issues with the driver, documentation, FPGA projects or application projects please contact us at:

support@earthpeopletechnology.com

and we will fix whatever problems arise. Or contact us and give us any questions you have about the DueProLogic, your projects, or any electronic question. At Earth People Technology, we are experienced embedded engineers and we live for this stuff. So, happy projecting, we look forward to hearing from you.

EPT Team

## How to Install the USB Driver for the DueProLogic under Windows 10

The DueProLogic uses the default FTDI VCP driver. You can either:
- Download directly from the FTDI website:
  https://www.ftdichip.com/Drivers/CDM/CDM21228_Setup.zip

- Locate the Drivers/EPT_Serial_Driver folder on the DVD included with the DueProLogic
- Download the project DVD from the Earth People website https://www.earthpeopletechnology.com/wp-content/uploads/2015/08/DUEPROLOGIC_USB_FPGA_PROJECT_4.0_DVD.zip

After download, follow the tasks to install the driver. After these tasks are successfully completed, you can proceed with the following sections.

## Install the Development Software

- Go to Section 2 of the DPL_FPGA_DEV_SYS_UM.pdf User Manual and download and install the Quartus Prime Lite software
- Go to Section 2 of the DPL_FPGA_DEV_SYS_UM.pdf User Manual and install the EPT_Blaster driver.
- Go to Section 2 of the DPL_FPGA_DEV_SYS_UM.pdf User Manual and download and install the Visual C# Express software.